Amendments to the Speficiation:

Please replace paragraph **[0010]** with the following amended paragraph:

BRIEF DESCRIPTION OF THE DRAWINGS

[0010]   Figure 1 is a block diagram of a USB connected mass storage drive showing data flow on the USB bus;

Figure 2 is a block diagram of a USB mass storage bridge controller according to the present invention;

~~Figure 3 is a state diagram~~ Figures 3A and 3B are state diagrams of the state machine used to detect a Command Block Wrapper (CBW);

Figure 4 is a circuit diagram of a portion of the parameter selection logic circuit for the state machine of Figure 3;

~~Figure 5 is a circuit diagram~~ Figures 5A and 5B are circuit diagrams of the remainder of the parameter selection logic circuit for the state machine of Figure 3; and

Figures 6 and 7 are state machine diagrams for computer program of the software controlled microcontroller.

Please replace paragraph **[0036]** with the following amended paragraph:

[0036]   Figures 6 and 7 show an ATA/ATAPI command state machine which is used to send ATA or ATAPI commands to the ATA or ATAPI devices. Figure 6 shows the first portion of the machine generally as 600 and Figure 7 shows the second portion of the state machine generally as 700. The Figures are connected to each other by connection points [A and B] E and F. The ATA/ATAPI command state machine utilizes the information in task_file0 to task_file15 as the command parameters. If the dev_sel is equal to 0, the command will select device 0 and if the dev_sel is equal to 1, the command will select

device 1. If the signal lba48_en is equal to 1, a 48 bit LBA address is used to address the ATA hard drive. However, sixteen bits of the 48 bit address are filled with 0's because the CBW command only contains a 32 bit address. If the signal lba48_en is equal to 0, a 28 bit LBA address is used to address the ATA hard drive. If the dma_mode signal is equal to 1, a DMA data transfer will be used for the transfer phase. The determination of whether Multiword DMA or Ultra DMA would be utilized is determined during the device configuration.